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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/522,847	01/28/2005	Martin J. Edwards	GB020119US	9356	
24738 PHILIPS ELE	7590 06/03/200 CTRONICS NORTH A	EXAM	EXAMINER		
INTELLECTUAL PROPERTY & STANDARDS 370 W. TRIMBLE ROAD MS 91/MG SAN JOSE, CA 95131			CARTER III, ROBERT E		
			ART UNIT	PAPER NUMBER	
,			2629	•	
			MAIL DATE	DELIVERY MODE	
			06/03/2008	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)	
10/522,847	EDWARDS ET AL.	
Examiner	Art Unit	
ROBERT E. CARTER III	2629	

	ROBERT E. CARTER III	2629	
The MAILING DATE of this communication appe	ars on the cover sheet with the	correspondence add	ress
THE REPLY FILED 05 May 2008 FAILS TO PLACE THIS APPI	ICATION IN CONDITION FOR AL	LOWANCE.	
 M The reply was filed after a final rejection, but prior to or on application, applicant must timely file one of the following i application in condition for allowance; (2) a Notice of Appe for Continued Examination (RCE) in compliance with 37 C periods: 	the same day as filing a Notice of eplies: (1) an amendment, affidavi al (with appeal fee) in compliance	Appeal. To avoid abar t, or other evidence, w with 37 CFR 41.31; or	hich places the (3) a Request
a) The period for reply expiresmonths from the mailing	date of the final rejection.		
b) A The period for reply expires on: (1) the mailing date of this Ar no event, however, will the statutory period for reply expire la Examiner Note: If box 1 is checked, check either box (a) or (I MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f	ter than SIX MONTHS from the mailing b). ONLY CHECK BOX (b) WHEN THE	date of the final rejection	n.
Extensions of time may be obtained under 37 CFR 1.136(a). The date of		36(a) and the appropriat	extension fee
have been filed is the date for purposes of determining the period of extunder 37 CFR 1.17(a) is calculated from: (1) the expiration date of the set forth in (b) above, if checked. Any reply received by the Office later may reduce any earned patent term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL.	ension and the corresponding amount hortened statutory period for reply origi	of the fee. The appropria nally set in the final Office	ate extension fee e action; or (2) as
 The Notice of Appeal was filed on A brief in compl filing the Notice of Appeal (37 CFR 41.37(a)), or any exter Notice of Appeal has been filed, any reply must be filed wi 	sion thereof (37 CFR 41.37(e)), to	avoid dismissal of the	
<u>AMENDMENTS</u>			
 The proposed amendment(s) filed after a final rejection, be They raise new issues that would require further core They raise the issue of new matter (see NOTE below 	sideration and/or search (see NO		cause
 (c) They are not deemed to place the application in bett appeal; and/or 	er form for appeal by materially red	ducing or simplifying th	ne issues for
(d) ☐ They present additional claims without canceling a c		ected claims.	
NOTE: <u>See Continuation Sheet</u> . (See 37 CFR 1.11			
 The amendments are not in compliance with 37 CFR 1.12 		mpliant Amendment (I	PTOL-324).
Applicant's reply has overcome the following rejection(s):			
 Newly proposed or amended claim(s) would be all non-allowable claim(s). 		•	
7. For purposes of appeal, the proposed amendment(s): a) how the new or amended claims would be rejected is prov The status of the claim(s) is (or will be) as follows:		I be entered and an e	planation of
Claim(s) allowed:			
Claim(s) objected to:			
Claim(s) rejected: <u>1.2 and 4-21</u> . Claim(s) withdrawn from consideration:			
AFFIDAVIT OR OTHER EVIDENCE			
The affidavit or other evidence filed after a final action, but because applicant failed to provide a showing of good and was not earlier presented. See 37 CFR 1.116(e).			
 The affidavit or other evidence filed after the date of filing entered because the affidavit or other evidence failed to or showing a good and sufficient reasons why it is necessary 	vercome <u>all</u> rejections under appea	al and/or appellant fail:	to provide a
10. The affidavit or other evidence is entered. An explanation REQUEST FOR RECONSIDERATION/OTHER	n of the status of the claims after er	ntry is below or attach	ed.
 The request for reconsideration has been considered but <u>See Continuation Sheet.</u> 	does NOT place the application in	condition for allowan	ce because:
12. Note the attached Information <i>Disclosure Statement(s)</i> . (13. Other:	PTO/SB/08) Paper No(s)		
0			
/Sumati Lefkowitz/ Supervisory Patent Examiner, Art Unit 2629			

Continuation of 3. NOTE: does NOT place the application in condition for allowance because: The proposed amendment to the claims moves dependent claim 7 from depending upon independent claim 10 to depending upon independent claim 1. Independent claims 10 and 1 are not identical in scope and therefore the amendment changes the scope of claim 7 such that further search and consideration is required.

Continuation of 11, does NOT place the application in condition for allowance because: Regarding applicant's argument that the prior art references do not teach the timing of the otta signals to the gates of the switching transistors being dependent upon at least one input that is connected to a switching transistor through which the input may be routed to a pixel element, the examiner disagrees. The examiner cled the timing of the routing of the data signals being dependent upon input signal 51. Mural teaches that signal 51 enables and disables the low power mode [0032], Mural further teaches that when the low power mode is off, the two instals 3 and 1012 to the pixel element are connected to the same electrical potential [0029], and the data signal on the horizontal scanning line 1 is switched from a low power sonal (Fig. 4, #b) to a full-power signal (Fig. 3, #b).

Therefore, there is a direct correlation between the state of the low power circuit, which is determined by input line 51, and the type of signals on the two input lines 3 and 1012, and horizontal scanning line 1, so the timing of the routing of the data signals to the gates of the switching transistors is dependent upon input line 51, the timing of which is directly correlated to the timing of at least one input that is connected to a switching transistor through which the input may be routed to a pixel element.